

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Trung M. Tran PATENT APPLICATION

Serial No.: 10/083,229 Group Art Unit: Unassigned

Filing Date: February 26, 2002 Examiner: Unassigned

For: METHOD, APPARATUS AND COMPUTER PRODUCT TO ORGANIZE DATA ON A DISPLAY AND FACILITATE TESTING OF AN INTEGRATED CIRCUIT DESIGN

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents Washington, D.C. 20231 RECEIVED

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Sir:

The following information is submitted in compliance with Applicants' duty of disclosure under 37 C.F.R. § 1.56. Form PTO-1449 and a copy of each reference recited below accompanies this document. It is respectfully requested that the cited information be expressly considered during the prosecution of this application, and the references be made of record therein and appear among the "references cited" on any patent to issue therefrom.

ISSUED PATENTS

Patent Number	Inventor	Grant Date		
5,634,002	Polk et al.	May 27, 1997		
5,867,162	O'Leary et al.	Feb. 2, 1999		
5,963,950	Nieslen et al.	Oct. 5, 1999		
6,069,625	Nielsen	May 30, 2000		
6,275,225	Rangarajan et al.	Aug. 14, 2001		



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Respectfully Submitted,

Kenneth C. Brooks Reg. No. 38,393

Examiner

Include copy of this form with next communication to applicant.

PTO/SB/08A (08-00)

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Sheet	1	of	1

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Application Number	10/083,229				
Filing Date	February 26, 2002				
First Named Invent r	Trung M. Tran				
Group Art Unit	Unassigned				
Examiner Name	Unassigned				
Attorney Docket Number	P6537/SUN-01-03				

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